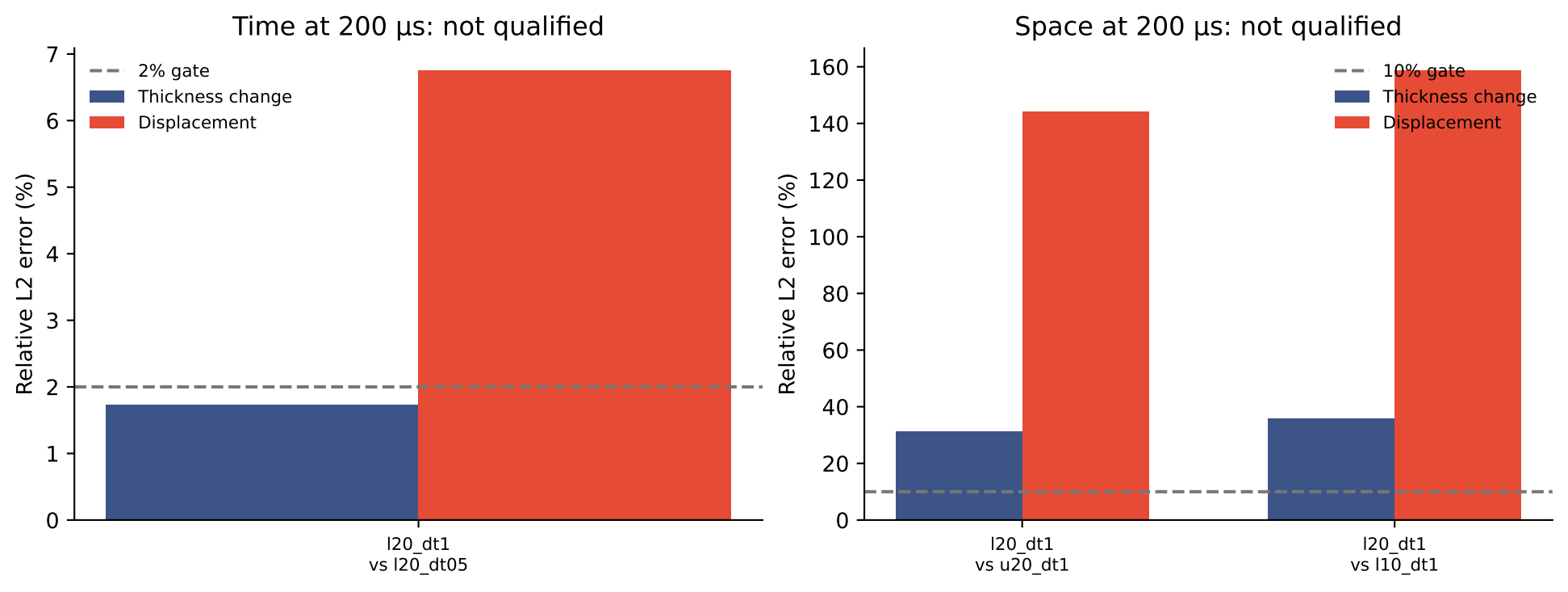

Time at 200 µs: not qualified
Relative L2 error (%)
2% gate
Thickness change
Displacement
0
1
2
3
4
5
6
7
l20_dt1
vs l20_dt05
Space at 200 µs: not qualified
Relative L2 error (%)
10% gate
Thickness change
Displacement
0
20
40
60
80
100
120
140
160
l20_dt1
vs u20_dt1
l20_dt1
vs l10_dt1